

Seal5 Development & User Meeting

06.12.2024

Agenda (06.12.2024)

- Sync on ongoing work
 - DLR:
 - Seal5 Reports
 - Support Register Pairs
 - TUM:
 - LLVM 19 migration
 - Core-V Fixes
 - PatternGen improvements
- Feature requests
 - Automatically generate intrinsics (without YAML) ([#69](#)) → optionally via `[[gen_intrinsic]]` attribute or seal5 settings
 - Intrinsics/Builtins for Memory Instructions ([#150](#)) → Come up with example and check required patches (ptr types, predicates)
 - Implement CDSL extern functions for common LLVM-IR intrinsics ([#149](#))
- Next meeting dates
 - **Internal:** Thu. 12+19.12.2024, 11:00 (Skype) → drop: 26.12. & 02.01.
 - **Public:** Thu. 09.01.2025, 11:00 (Zoom)